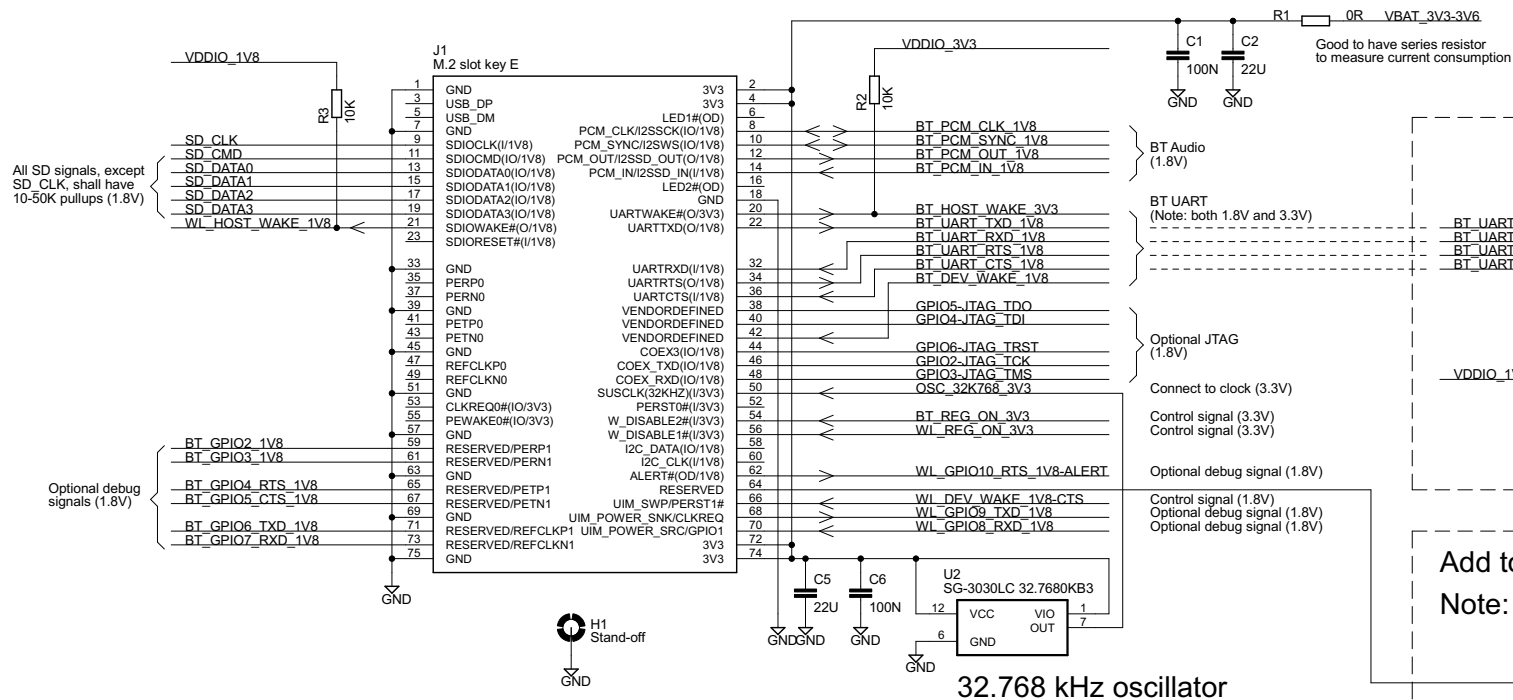
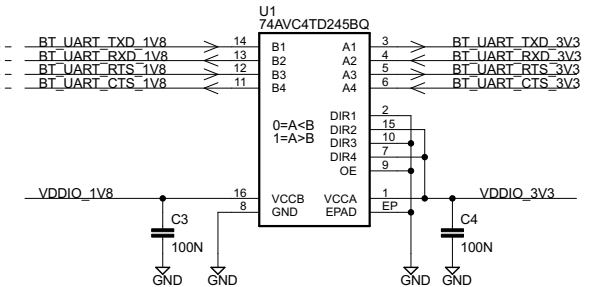


M.2 Key E Connector with SDIO Interface



Standoff for M.2 connector,
placed at 30mm distance from connector

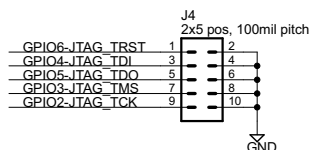
Example level shifter



Add to override 1.8V VDDIO to 3.3V
Note: not working on all M.2 modules

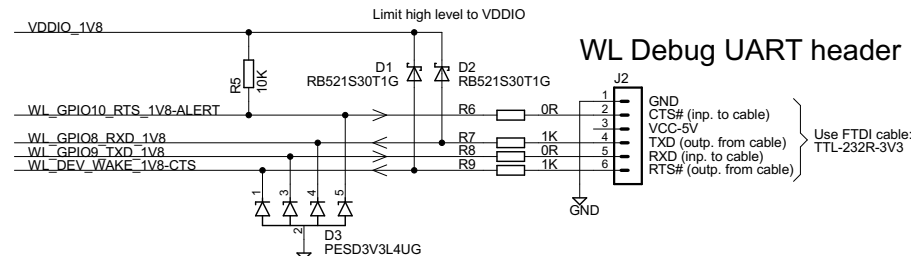
R4 0R VDDIO_3V3

WLAN JTAG

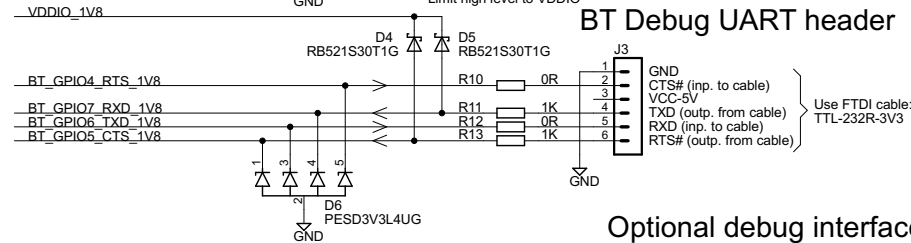


Optional debug interface

WL Debug UART header



BT Debug UART header



Optional debug interface



(C) Embedded Artists AB

TITLE: M.2 SDIO Interface rev PA1

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